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June 24, 2004

Commissioner for Patents
PO Box 1450
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Re: U.S. Utility Patent Application
Application No. 09/882,285; Filed: June 18, 2001
For: **Method and Apparatus for Saving and Restoring Processor Register
Values and Allocating and Deallocating Stack Memory**
Inventors: Kissell *et al.*
Our Ref: 1778.0210000

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JUN 29 2004

Technology Center 2100

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Petition for Extension of Time Under 37 C.F.R. § 1.136(a)(1);
2. Request for Continued Examination (RCE) Transmittal (PTO/SB/20);
3. Fee Transmittal (PTO/SB/17);
4. Amendment and Reply Under 37 C.F.R. § 1.111;
5. First Supplemental Information Disclosure Statement;
6. Form PTO-1449 (4 pages) citing 9 documents;
7. Copies of the cited documents;
8. Credit Card Payment Form (PTO-2038); and
9. One (1) return postcard.

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier.

Commissioner for Patents

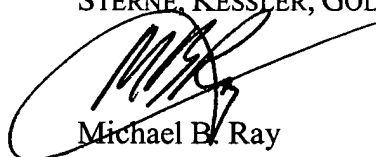
June 24, 2004

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The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

A handwritten signature in black ink, appearing to read "MBR", is written over a large, loopy circular flourish.

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MBR/LMY:krd
Enclosures

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